

**Schematic/PLD translation**

So users can supply schematics as input to the \$495 Tango-PLD programmable logic compiler. Ver 1.20 comes with the SCH2PLD translator, which supports both the firm's Tango-Schematic Series II as well as OrCad's /SDT (Ver 3.2 or later) packages. Additional Tango-PLD libraries comprise 42 TTL, SSI and MSI components, 35 generic AND, NAND, NOR and OR gates as well as seven miscellaneous parts including ports and power/ground symbols. The PLD design tool also adds support for powerup reset, an option for disabling logic minimization, and 13 devices from Intel, AMD, National/TI, Lattice and Altera: with these models, the program supports more than 125 PAL, GAL and PEEL devices.

*Accel Technologies Inc* (San Diego, CA (619) 554-1000). Circle VIP No 113

**68HC001 emulation**

Supporting the Motorola 68HC001 microprocessor in both 8- and 16-bit modes, the HMI-200-68000 provides realtime emulation, four complex break and trigger points, two 4k x 72-bit trace buffers including 16 external trace bits and 32 bits of time tag information. A basic emulator for the 68000 costs \$7500 and the HC68C001 adaptor adds \$950. The standard configuration contains 256k bytes of memory with an option for 1M byte. An optional program is the \$1500 SourceGate windowed high-level language debugger, which supports C, Pascal and PL/M compilers from more than a dozen manufacturers.

*Huntsville Microsystems Inc* (Huntsville, AL (205) 881-6005). Circle VIP No 114

**Symbol libraries**

Users can quickly place, modify, rotate and scale schematic symbols within the Generic CADD Starter Kit and Generic CADD 5.0 drawing packages with the aid of electronic symbol libraries. A \$100 pc-board library consists of 576 symbols that allow you to create photo-ready designs with a laser printer or high-resolution dot-matrix printer. Included in the selection are hole patterns and silkscreen components for ICs, connectors, discretes and miscellaneous fingers and switches.

*Generte Software Inc* (Bothell, WA (206) 487-CADD). Circle VIP No 115

**i960 emulator**

Operating at 33 MHz and offering a debugger that interfaces with Windows 3.0, the Step Express Plus (\$28,750) provides hardware/software debug capability for the Intel i960 32-bit embedded RISC processor. Communication between the emulator and a host PC is over a parallel bus extension, and you can develop code on the PC or another system through the PC; for networked environments, the emulator supports the TCP/IP interface. The target processor remains in the system to eliminate the possibility of induced interference from high-speed signals traveling along the cable. While the system executes code, the emulator passively monitors all i960 bus activity and records it in trace memory. The Express Plus reconstructs programs executing from the processor's cache by using all the control information supplied by the i960 and emulator software—unlike other emulator/processor combinations that only record bus activity and whose instruction buffers, caches and other microprocessor features mask the actual execution sequence.

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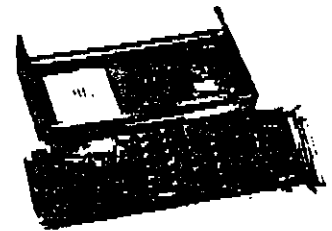
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